



HannStar Display Corp.

Document Title	HSD280MUW3 Standard Specification	Page No.	1 / 29
Document No.	DC130-004707	Revision	1.0

To :

Date :

Customer Acceptance Specification

Model : **HSD280MUW3**
- A++

Accepted by:

Signature

Date

Note:

1. Please contact HannStar Display Corp. before designing your product based on this module specification.
2. The information contained herein is presented merely to indicate the characteristics and performance of our products. No responsibility is assumed by HannStar for any intellectual property claims or other problems that may result from application based on the module described herein.

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HannStar Display Corp.

Document Title	HSD280MUW3 Standard Specification	Page No.	2 / 29
Document No.	DC130-004707	Revision	1.0

Record of Revisions

Rev.	Date	Sub-Model	Description of change
1.0	May, 11, 2010	A++	Formal Product specification for HSD280MUW3-A was first issued.

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HannStar Display Corp.

Document Title	HSD280MUW3 Standard Specification	Page No.	3 / 29
Document No.	DC130-004707	Revision	1.0

Contents

1.0	General Descriptions	p.4
2.0	Absolute Maximum Ratings	p.5
3.0	Optical Characteristics	p.7
4.0	Block Diagram	p.11
5.0	I/O Connection Pin Assignment	p.15
6.0	Electrical Characteristics	p.16
7.0	Outline Dimension	p.24
8.0	Lot Mark	p.26
9.0	Package Specification	p.27
10.0	General Precaution	p.28

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HannStar Display Corp.

Document Title	HSD280MUW3 Standard Specification	Page No.	4 / 29
Document No.	DC130-004707	Revision	1.0

1.0 GENERAL DESCRIPTIONS

1.1 Introduction

HannStar Display model **HSD280MUW3-A**** is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a WUXGA resolution (1920 vertical by 1200 horizontal pixel array) TFT LCD panel, the voltage reference, common voltage, DC-DC converter, column, and row driver circuit.

1.2 Features

- 28" (27.54") WUXGA TN mode TFT LCD panel
- High speed response time
- Supported WUXGA (H: 1920 pixels, V: 1200 lines) resolution
- With LCD Timing Controller
- RoHS compatible

1.3 General information

Item	Specification		Unit
Outline dimension	629.0× 417.0× 39.75 (typ.)		mm
Display area	593.28 (H) x370.8 (V)		mm
Screen Diagonal	27.54		inch
Number of Pixel	1920(H) x 1200(V)		Pixels
Pixel pitch	0.309(H) x 0.309(V)		mm
Pixel arrangement	RGB Vertical stripe		
Display color	16.7M (6-bit+HiFRC)		
Display mode	Normally white		
Surface treatment	Antiglare, Hard-Coating (3H)		
Response Time	Tr + Tf	5 (TYP.)	Msec
Weight	4350		G
Input signal	2-ch LVDS		
Power consumption	Logic system	6.2 (TYP.)	W
	B/L system	54.6 (TYP.)	W

1.4 Applications

- Desktop and Multi-function monitors
- Display terminals for AV applications
- Monitors for industrial applications

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Document Title	HSD280MUW3 Standard Specification	Page No.	5 / 29
Document No.	DC130-004707	Revision	1.0

1.5 Mechanical Information

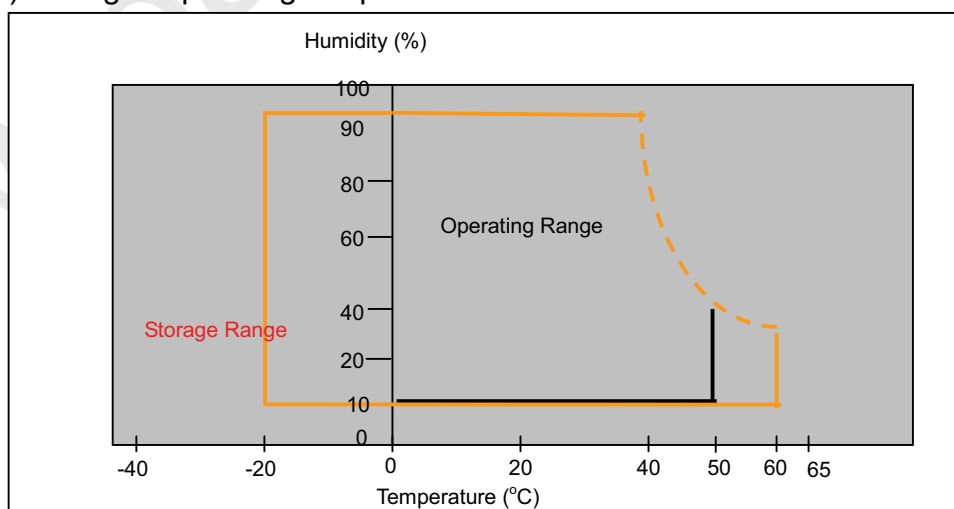
Item		Min.	Typ.	Max.	Unit
Module Size	Horizontal(H)	628.0	629.0	630.0	mm
	Vertical(V)	416.0	417.0	418.0	mm
	Depth(D)	39.25	39.75	40.25	mm
Weight (Without Inverter)		4100	4350	4600	g
Torque of customer screw hole				3.5	Kg*m

2.0 ABSOLUTE MAXIMUM RATINGS

2.1 Absolute Rating of Environment

Item	Symbol	Min.	Max.	Unit	Note
Storage temperature	T _{STG}	-20	60	°C	
Operating temperature	T _{OPR}	0	50	°C	(1)
Vibration (non-operating)	V _{NOP}	--	1.5	G	(2)
Shock (non-operating)	S _{NOP}	--	50	G	(3)
Storage humidity	H _{STG}	10	90	%RH	(3)
Operating humidity	H _{OP}	10	80	%RH	(4)
Low pressure (operating)	P _{LOP}	697	--	HPa	(5)
Low pressure (non-operating)	P _{LNOP}	116	--	HPa	(6)

Note (1) Storage /Operating temperature



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HannStar Display Corp.

Document Title	HSD280MUW3 Standard Specification	Page No.	6 / 29
Document No.	DC130-004707	Revision	1.0

- (2) 10-500Hz sine wave, X,Y,Z each directions, 30min/cycle.
- (3) 11ms, $\pm X$, $\pm Y$, $\pm Z$ direction, one time each. For this shock test,
It is necessary to fill the silicon rubber between the shock jigs as buffer.
- (4) Max wet bulb temp. $\approx 39^{\circ}\text{C}$
- (5) 2 hrs. (10000 feet)
- (6) 24hrs. (50000 feet)

2.2 Electrical Absolute Rating:

2.2.1 TFT LCD Module:

Item	Symbol	Min.	Max.	Unit.	Note
Power supply Voltage	VDD	-0.3	5.5	V(DC)	(1)(2)

2.2.2

Back Light Unit:

Item	Symbol	Min.	Max.	Unit	Note
Lamp current	I_L	3.0	12	mA	(1)(2)(3)
Lamp frequency	f_L	40	60	KHz	(1)(2)(3)

Note: (1) Permanent damage may occur to the LCD module if beyond this specification.
Functional operation should be restricted to the conditions described under
Normal Operating Conditions.
(2) Within $T_a = 25 \pm 2^{\circ}\text{C}$

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HannStar Display Corp.

Document Title	HSD280MUW3 Standard Specification	Page No.	7 / 29
Document No.	DC130-004707	Revision	1.0

3.0 OPTICAL CHARACTERISTICS

3.1 Optical specification

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast		CR	$\theta=0^{\circ}$ $\phi=0^{\circ}$ Normal viewing	600	800	--		(1)(2)
Response time	Rising	TR		--	5 (Tr+Tf)	10 (Tr+Tf)	msec	(1)(3)
	Falling	TF		--				
White luminance (center of screen)		Y _L		320	400		cd/m ²	(1)(4)(7) (IL=10.5 mA)
Color chromaticity (CIE1931)	Red	R _x		0.615	0.645	0.675		(1)(5)
		R _y		0.305	0.335	0.365		
	Green	G _x		0.272	0.302	0.332		
		G _y		0.594	0.624	0.654		
	Blue	B _x		0.112	0.142	0.172		
		B _y		0.034	0.064	0.094		
	White	W _x	0.283	0.313	0.343			
		W _y	0.299	0.329	0.359			
Viewing angle	Hor.	θ _L	CR>10	75	85	--		
		θ _R		75	85	--		
	Ver.	θ _H		70	80	--		
		θ _L		70	80	--		
Brightness uniformity		B _{UNI}	$\theta=0^{\circ}$ $\phi=0^{\circ}$	75	--	--	%	(6)

3.2 Measuring Condition

- Measuring surrounding: dark room
- Lamp current I_{BL}: 10.5mA, Inverter: ITC 52284U0TH
- V_{DD1}=5.0V, I_{bl}=10.5mA, f_V=60Hz, f_{DCLK}=77MHz
- Surrounding temperature: 25±2°C
- 30min. Warm-up time.

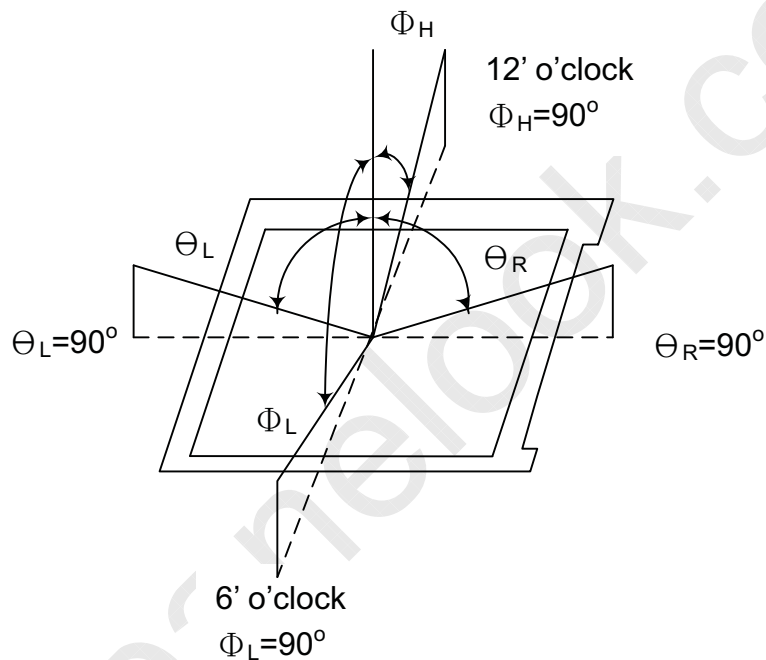
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Document Title	HSD280MUW3 Standard Specification	Page No.	8 / 29
Document No.	DC130-004707	Revision	1.0

3.3 Measuring Equipment

- FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.
- Measuring spot size : 20~21mm

Note (1) Definition of Viewing Angle:



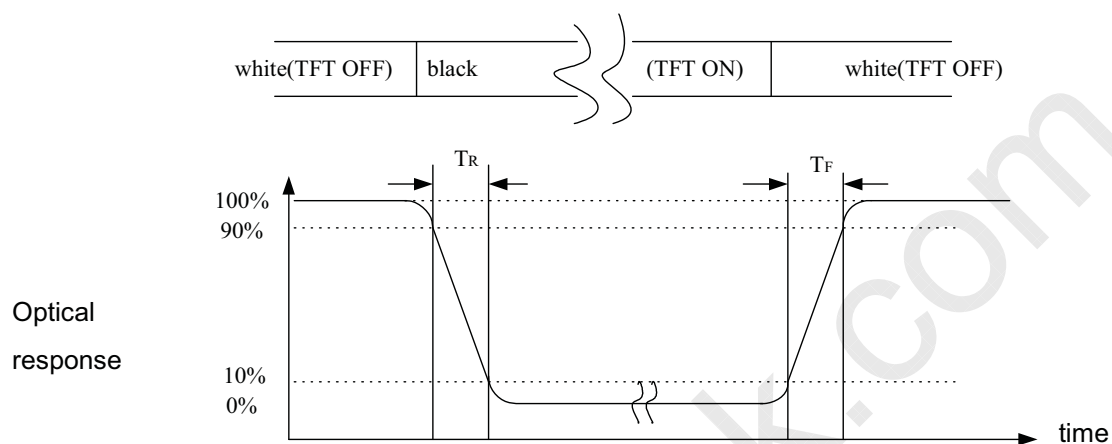
Note (2) Definition of Contrast Ratio(CR) :
measured at the center point of panel

$$CR = \frac{\text{Luminance with all pixels white (L255)}}{\text{Luminance with all pixels black (L0)}}$$

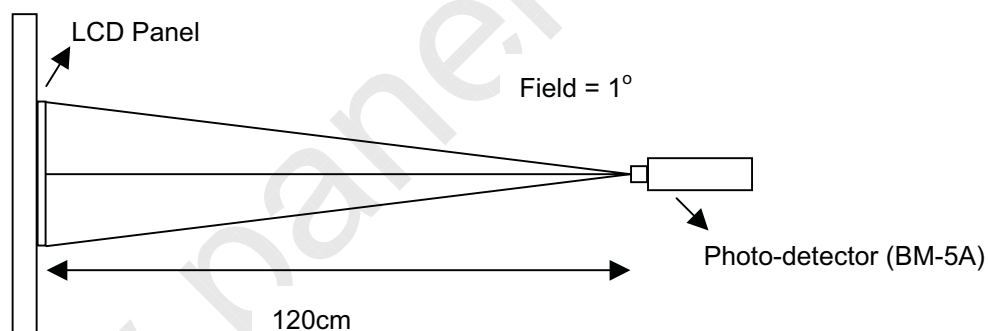
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Document Title	HSD280MUW3 Standard Specification	Page No.	9 / 29
Document No.	DC130-004707	Revision	1.0

Note (3) Definition of Response Time: Sum of T_R and T_F



Note (4) Optical characteristic measurement setup

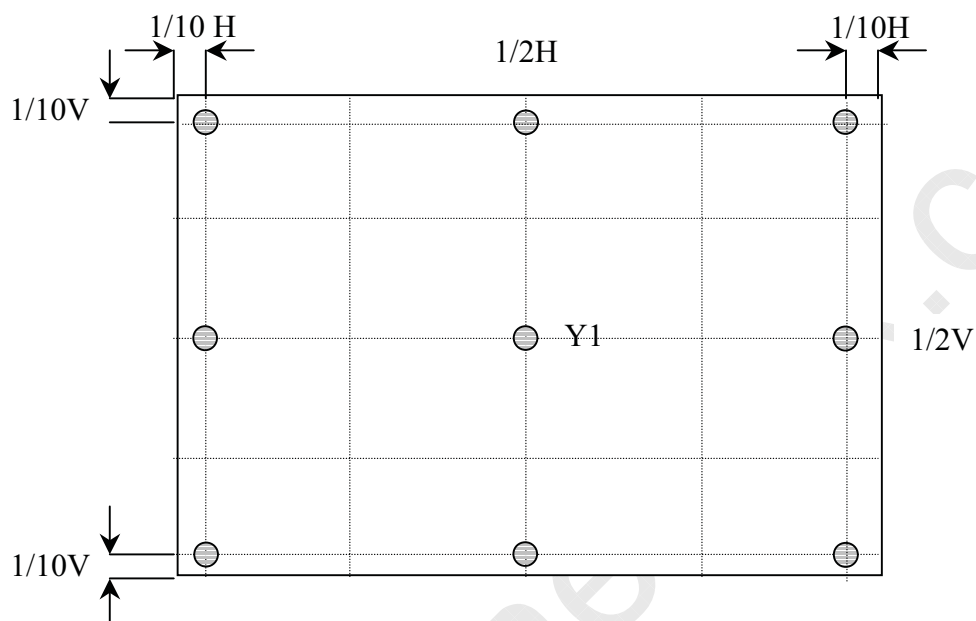


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Document Title	HSD280MUW3 Standard Specification	Page No.	10 / 29
Document No.	DC130-004707	Revision	1.0

Note (5) Definition of Center Luminance of White

Center Luminance= Y1



Note (6) Definition of brightness uniformity

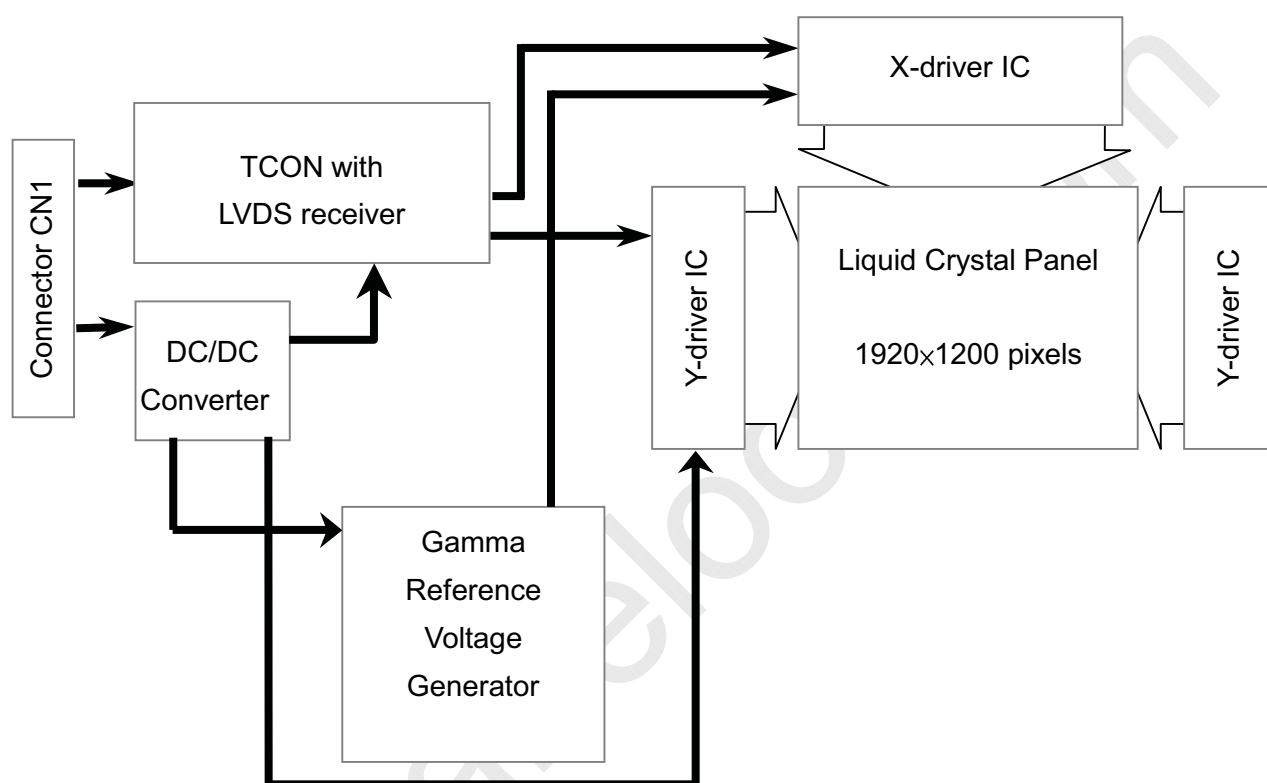
(Min Luminance of 9 points)

$$\text{Luminance uniformity} = \frac{\text{(Min Luminance of 9 points)}}{\text{(Max Luminance of 9 points)}} \times 100\%$$

Document Title	HSD280MUW3 Standard Specification	Page No.	11 / 29
Document No.	DC130-004707	Revision	1.0

4.0 BLOCK DIAGRAM

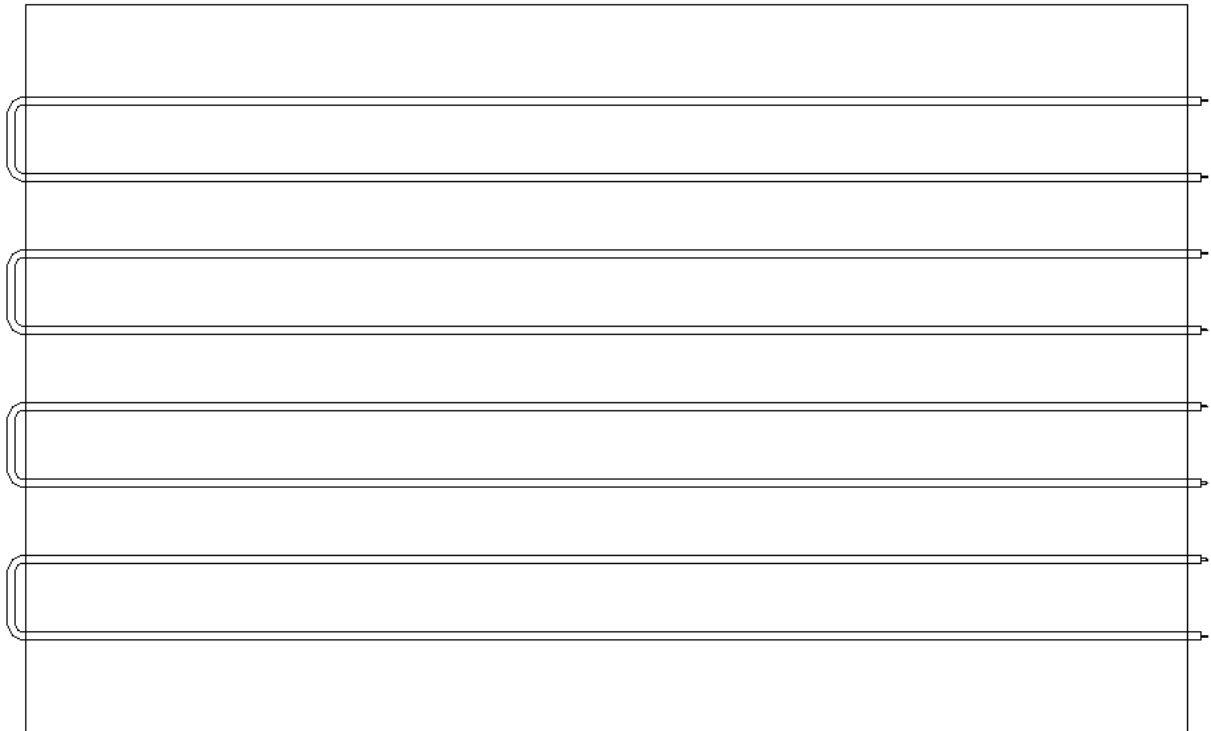
4.1 LCD Module Block Diagram:



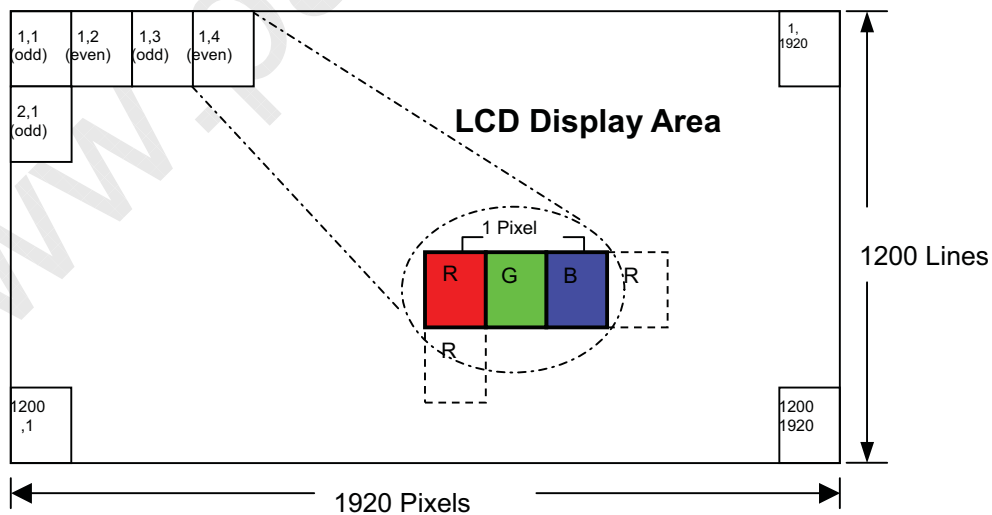
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Document Title	HSD280MUW3 Standard Specification	Page No.	12 / 29
Document No.	DC130-004707	Revision	1.0

4.2 Backlight Unit



4.3 Pixel Format



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HannStar Display Corp.

Document Title	HSD280MUW3 Standard Specification	Page No.	13 / 29
Document No.	DC130-004707	Revision	1.0

4.4 Relationship Between Displayed Color and Input

	Display	MSB	LSB	MSB	LSB	MSB	LSB	Gray scale			
		R R R R R R R R R R								Level	
		7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0						
Basic color	Black	L	L	L	L	L	L	L	L	-	
	Blue	L	L	L	L	L	L	L	L	-	
	Green	L	L	L	L	L	L	L	L	-	
	Light Blue	L	L	L	L	L	L	L	L	-	
	Red	H	H	H	H	H	H	H	H	-	
	Purple	H	H	H	H	H	H	H	H	-	
	Yellow	H	H	H	H	H	H	H	H	-	
	White	H	H	H	H	H	H	H	H	-	
Gray scale of Red	Black	L	L	L	L	L	L	L	L	L0	
	Dark	L	L	L	L	L	L	L	L	L	L1
		L	L	L	L	L	L	L	L	L	L2
		:	:	:	:	:	:	:	:	:	L3... L251
	Light	H	H	H	H	H	L	L	L	L	L255
		H	H	H	H	H	L	H	L	L	L255
		H	H	H	H	H	H	L	L	L	L255
	Red	H	H	H	H	H	H	H	H	Red L255	
	Gray scale of Green	Black	L	L	L	L	L	L	L	L	L0
Dark		L	L	L	L	L	L	L	L	L	L1
		L	L	L	L	L	L	L	L	L	L2
		:	:	:	:	:	:	:	:	:	L3... L251
Light		L	L	L	L	L	L	L	L	L	L255
		L	L	L	L	L	L	L	L	L	L255
		L	L	L	L	L	L	L	L	L	L255
Green		L	L	L	L	L	L	L	L	Green L255	
Gray scale of Blue		Black	L	L	L	L	L	L	L	L	L0
	Dark	L	L	L	L	L	L	L	L	L	L1
		L	L	L	L	L	L	L	L	L	L2

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HannStar Display Corp.

Document Title	HSD280MUW3 Standard Specification	Page No.	14 / 29
Document No.	DC130-004707	Revision	1.0

		↑	:	:	:	L3... L251
		↓	L L L L L L L L	L L L L L L L L	H H H H H H L L	L255
		Light	L L L L L L L L	L L L L L L L L	H H H H H H L H	L255
			L L L L L L L L	L L L L L L L L	H H H H H H H L	L255
	Blue		L L L L L L L L	L L L L L L L L	H H H H H H H H	Blue L255
	Gray scale of White & Black	Black	L L L L L L L L	L L L L L L L L	L L L L L L L L	L0
		Dark	L L L L L L L H	L L L L L L L H	L L L L L L L H	L1
			L L L L L L H L	L L L L L L H L	L L L L L L H L	L2
		↑	:	:	:	L3... L251
		Light	H H H H H H L L	H H H H H H L L	H H H H H H L L	L252
			H H H H H H L H	H H H H H H L H	H H H H H H L H	L253
			H H H H H H H L	H H H H H H H L	H H H H H H H L	L254
		White	H H H H H H H H	H H H H H H H H	H H H H H H H H	White L255

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HannStar Display Corp.

Document Title	HSD280MUW3 Standard Specification	Page No.	15 / 29
Document No.	DC130-004707	Revision	1.0

5.0 I/O CONNECTION PIN ASSIGNMENT

5.1 Interface Connector (30-pins) (JAE: FI-X30SSL-HF or equivalent)

Pin No.	Signal	Description
1	RinO0-	Receiver Signal (-)
2	RinO0+	Receiver Signal (+)
3	RinO1-	Receiver Signal (-)
4	RinO1+	Receiver Signal (+)
5	RinO2-	Receiver Signal (-)
6	RinO2+	Receiver Signal (+)
7	VSS	Ground
8	RinOC-	Clock Signal (-)
9	RinOC+	Clock Signal (+)
10	RinO3-	Receiver Signal (-)
11	RinO3+	Receiver Signal (+)
12	RinE0-	Receiver Signal (-)
13	RinE0+	Receiver Signal (+)
14	VSS	Ground
15	RinE1-	Receiver Signal (-)
16	RinE1+	Receiver Signal (+)
17	VSS	Ground
18	RinE2-	Receiver Signal (-)
19	RinE2+	Receiver Signal (+)
20	RinEC-	Clock Signal (-)
21	RinEC+	Clock Signal (+)
22	RinE3-	Receiver Signal (-)
23	RinE3+	Receiver Signal (+)
24	VSS	Ground
25	NC	SDA
26	NC	SCL
27	NC	NC
28	VDD+5V	Power Supply, 5V (Typical)
29	VDD+5V	Power Supply, 5V (Typical)
30	VDD+5V	Power Supply, 5V (Typical)

5.2 Back Light Unit (CCFL) Connectors:

CN2, 3, 4, 5...8: CCFL Power Source (JST BHR-04VS-1(4pin) or equivalent)

Pin No.	Symbol	Color	Function
1	High1	Pink	CCFL power supply (High voltage)
2	High2	White	CCFL power supply (High voltage)

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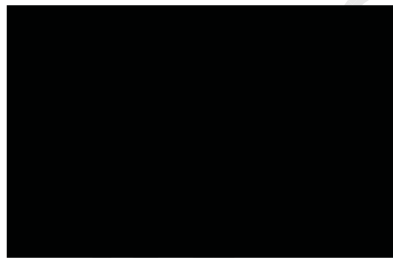
Document Title	HSD280MUW3 Standard Specification	Page No.	16 / 29
Document No.	DC130-004707	Revision	1.0

6.0 ELECTRICAL CHARACTERISTICS

6.1 TFT LCD Module:

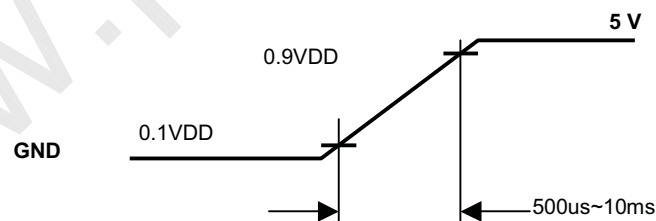
Item	Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of power supply	V_{DD}	4.5	5.0	5.5	V	
Current of power supply	I_{DD1}	760	955	1150	mA	(1)
Vsync frequency	f_V	48	60	75	Hz	(2)
Hsync frequency	f_H	59.28	74.10	92.62	KHz	
Frequency	f_{DCLK}	61.65	77.06	90.75	MHz	
Input rush current	I_{RUSH}	---	---	6	A	(3)

Note (1) Black pattern (L0):



Note (2) When f_v is too low, a flicker may be occurred on the display.

Note (3) Input Rush Current condition



Document Title	HSD280MUW3 Standard Specification	Page No.	17 / 29
Document No.	DC130-004707	Revision	1.0

6.2 Back-Light Unit

The backlight system is a direct-lighting type with 4U CCFLs (Cold Cathode Fluorescent Lamp). The characteristics of the lamp are shown in the following tables.

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Lamp current	IL	3	10.5	12	mA(rms)	(1)
Lamp voltage	VL	1170	1300	1430	V(rms)	$I_L=10.5mA$ A
Frequency	fL	40	50	60	KHz	(2)
Operating Lifetime	Hr	50,000	--	--	Hour	10.5mA(3)
Startup voltage	Vs	2300	--	--	V(rms)	at 25°C
		2800				at 0°C

6.2.1 Brightness Control Specification:

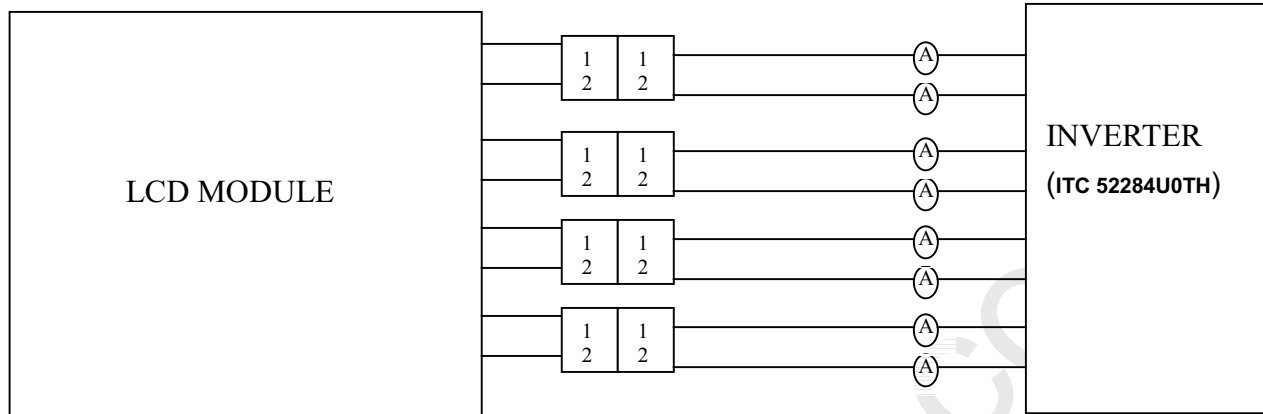
Item	Symbol	Min.	Max.	Unit	Remark
Output Frequency	fL	40	60	KHz	
Burst ON Duty Ratio	Dmin	10.0	100.0	%	
Burst On-Duty 100% Lamp current	I_L	9.0	12.0	mA	
Burst On-Duty 10% Lamp Current	I_L	3.0	--	mA	
Burst frequency	f _B	250	300	Hz	

Note : Above characteristics measured using Sumida Inverter (IV50160/T-LF).

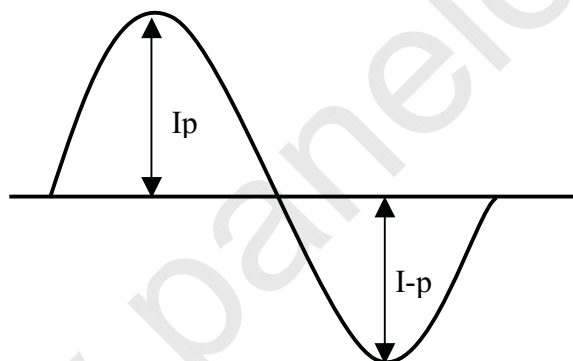
Note (1) Lamp life time (Hr) can be defined as the time in which it continues to operate under the condition: $T_a=25\pm3^{\circ}C$, typical lamp current until the brightness becomes less than 50%.

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Document Title	HSD280MUW3 Standard Specification	Page No.	18 / 29
Document No.	DC130-004707	Revision	1.0



- The asymmetry rate of the inverter waveform should be less than 10%.
- The distortion rate of the waveform should be within $\sqrt{2} \pm 10\%$.
- The inverter output waveform should be better similar to the ideal sine wave.



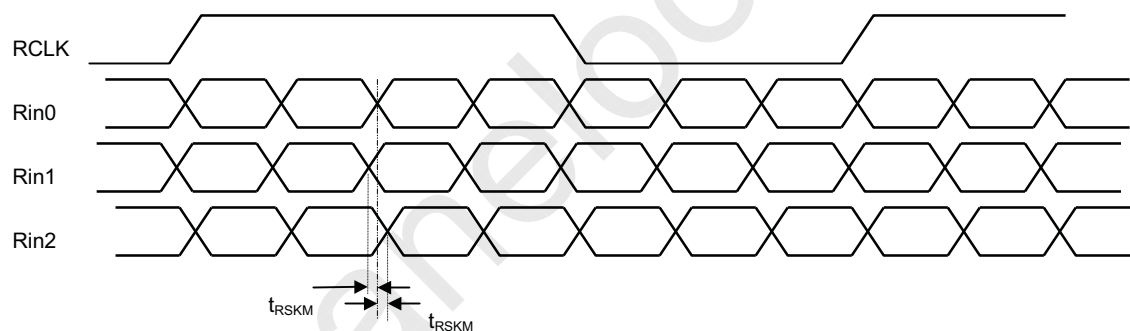
$$\text{Asymmetry rate} = |I_p - I-p| / I_{rms} \times 100\%$$

$$\text{Distortion rate} = I_p \text{ (or } I-p) / I_{rms}$$

Document Title	HSD280MUW3 Standard Specification	Page No.	19 / 29
Document No.	DC130-004707	Revision	1.0

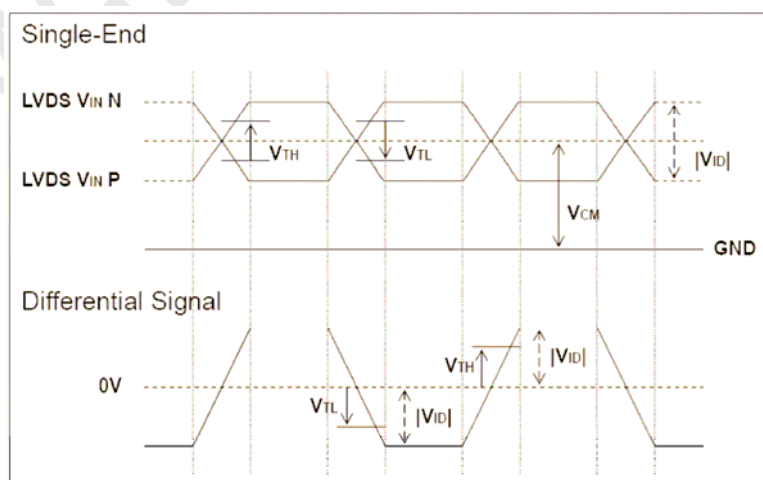
6.3 Switching Characteristics for LVDS Receiver

Item	Symbol	Min.	Typ.	Max.	Unit	Conditions
Differential Input High Threshold	V_{th}	—	—	100	mV	$V_{CM}=1.2V$
Differential Input Low Threshold	V_{tl}	-100	—	—	mV	
Input Current	I_{IN}	-10	—	10	μA	
Input Voltage Range(Signal ended)	V_{IN}	0	—	2.4	V	
Differential input Voltage	$ V_{ID} $	100	—	600	mV	
Common Mode Voltage Offset	V_{CM}	$ V_{ID} /2$	—	$2.4- V_{ID} /2$	V	
Clock Frequency	f_c	61	77	91	MHz	
LVDS Skew Margin	t_{RSKM}	—	—	200	pS	At $f_c=77MHz$
LVDS Input Clock Jitter Tolerance	—	—	—	± 2.5	%	center spread



LVDS Receiver skew margin

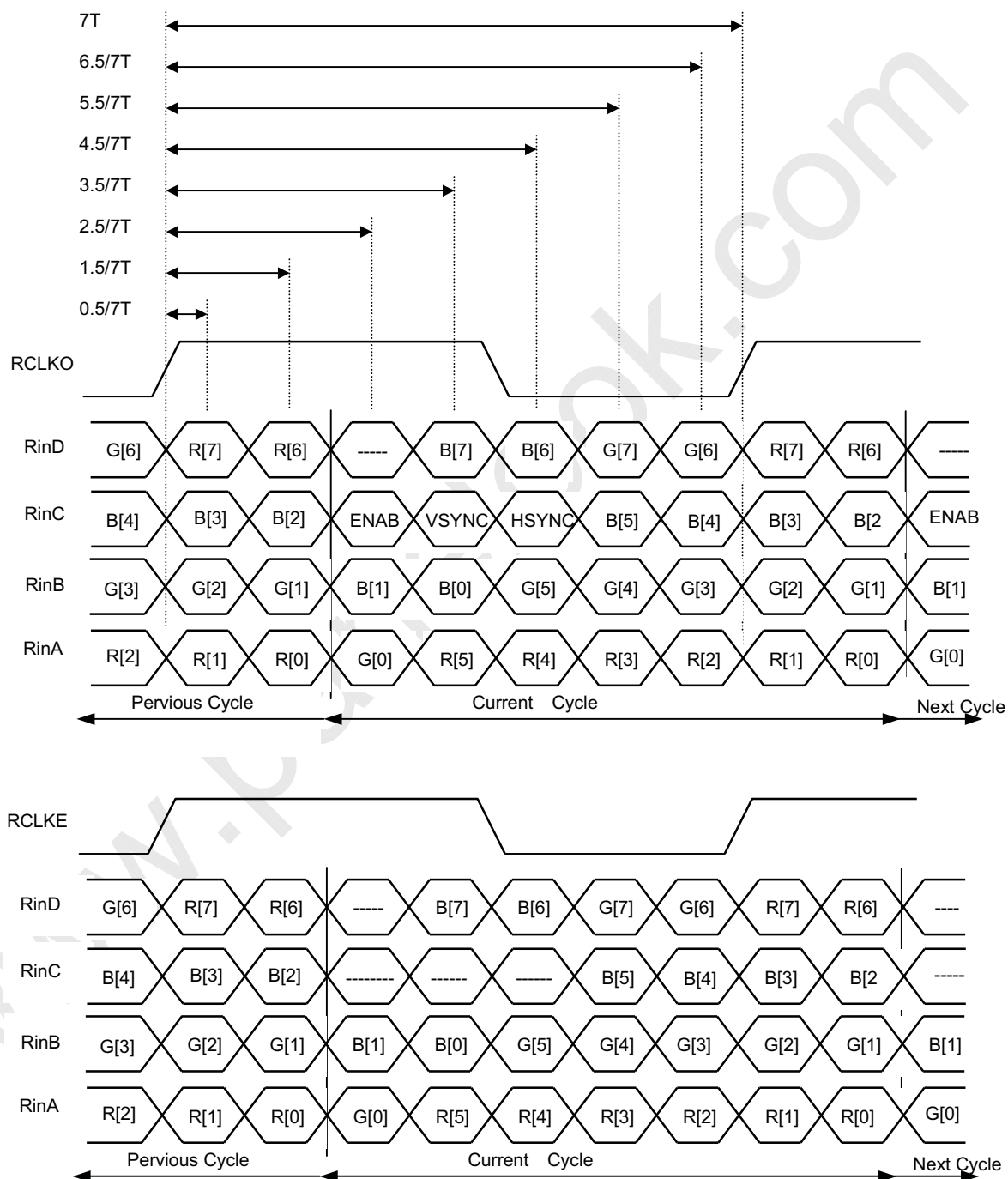
Differential Signal Diagram



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Document Title	HSD280MUW3 Standard Specification	Page No.	20 / 29
Document No.	DC130-004707	Revision	1.0

6.4 Bit Mapping & Interface Definition

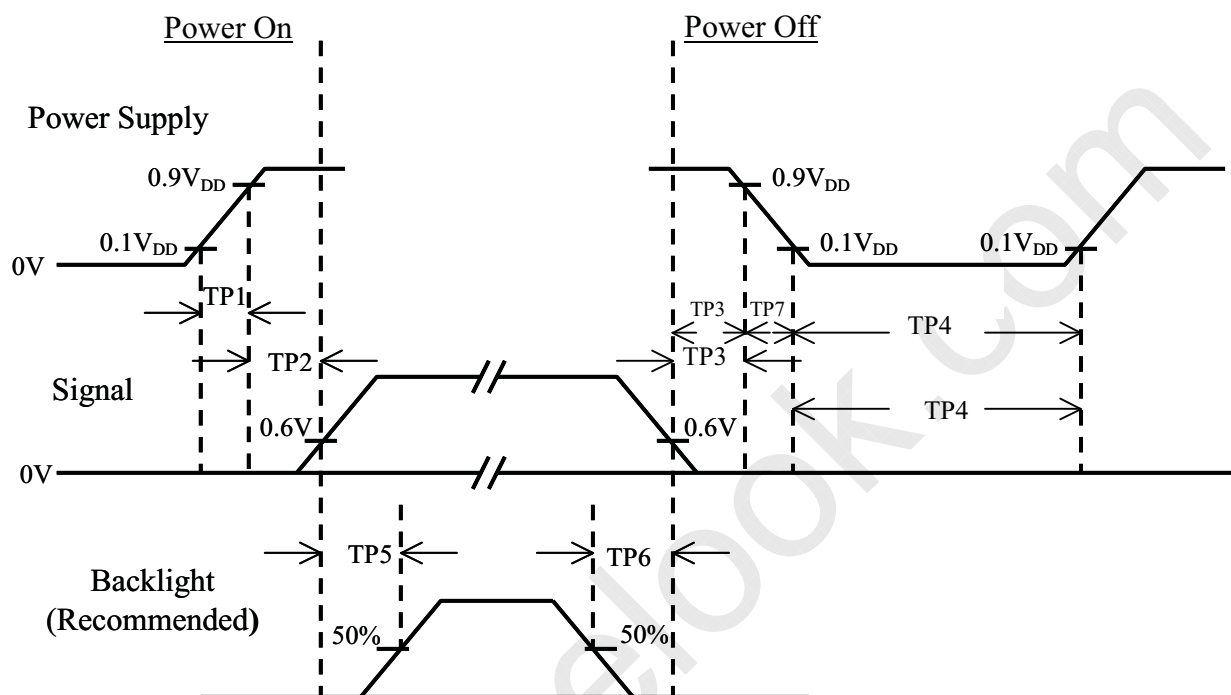


Bit Mapping & Timing Definition

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Document Title	HSD280MUW3 Standard Specification	Page No.	22 / 29
Document No.	DC130-004707	Revision	1.0

6.6 Power On / Off Sequence :



Item	Min.	Typ.	Max.	Unit	Remark
TP1	0.5	--	10	msec	
TP2	0.01	--	50	msec	
TP3	20	35	50	msec	
TP4	1000	--	--	msec	
TP5	200	--	--	msec	
TP6	200	--	--	msec	
TP7	1	--	10	msec	

Note : (1) The supply voltage of the external system for the module input should be the same as the definition of V_{DD}.

(2) Apply the lamp voltage within the LCD operation range. When the back-light turns on before the LCD operation or the LCD turns off before the back-light turns off, the display may momentarily become white.

(3) In case of V_{DD} = off level, please keep the level of input signal on the low or keep a high impedance.

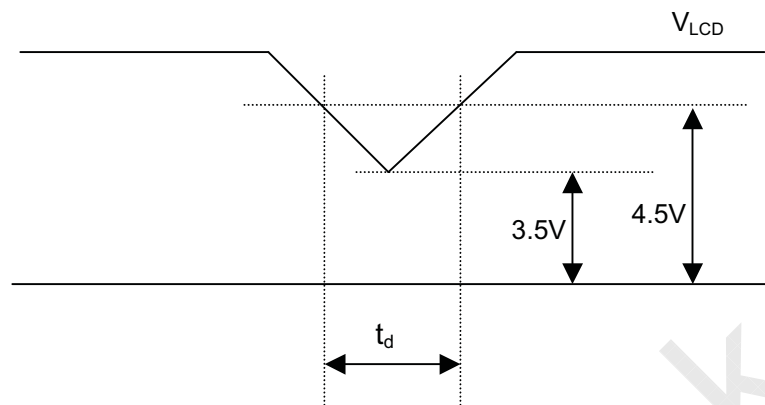
(4) TP4 should be measured after the module has been fully discharged between power off and on period.

(5) Interface signal shall not be kept at high impedance when the power is on.

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Document Title	HSD280MUW3 Standard Specification	Page No.	23 / 29
Document No.	DC130-004707	Revision	1.0

6.7 V_{LCD} Power Dip Condition :



Note : (1) Dip condition

$$3.5V < V_{LCD} \leq 4.5V, t_d \leq 20ms$$

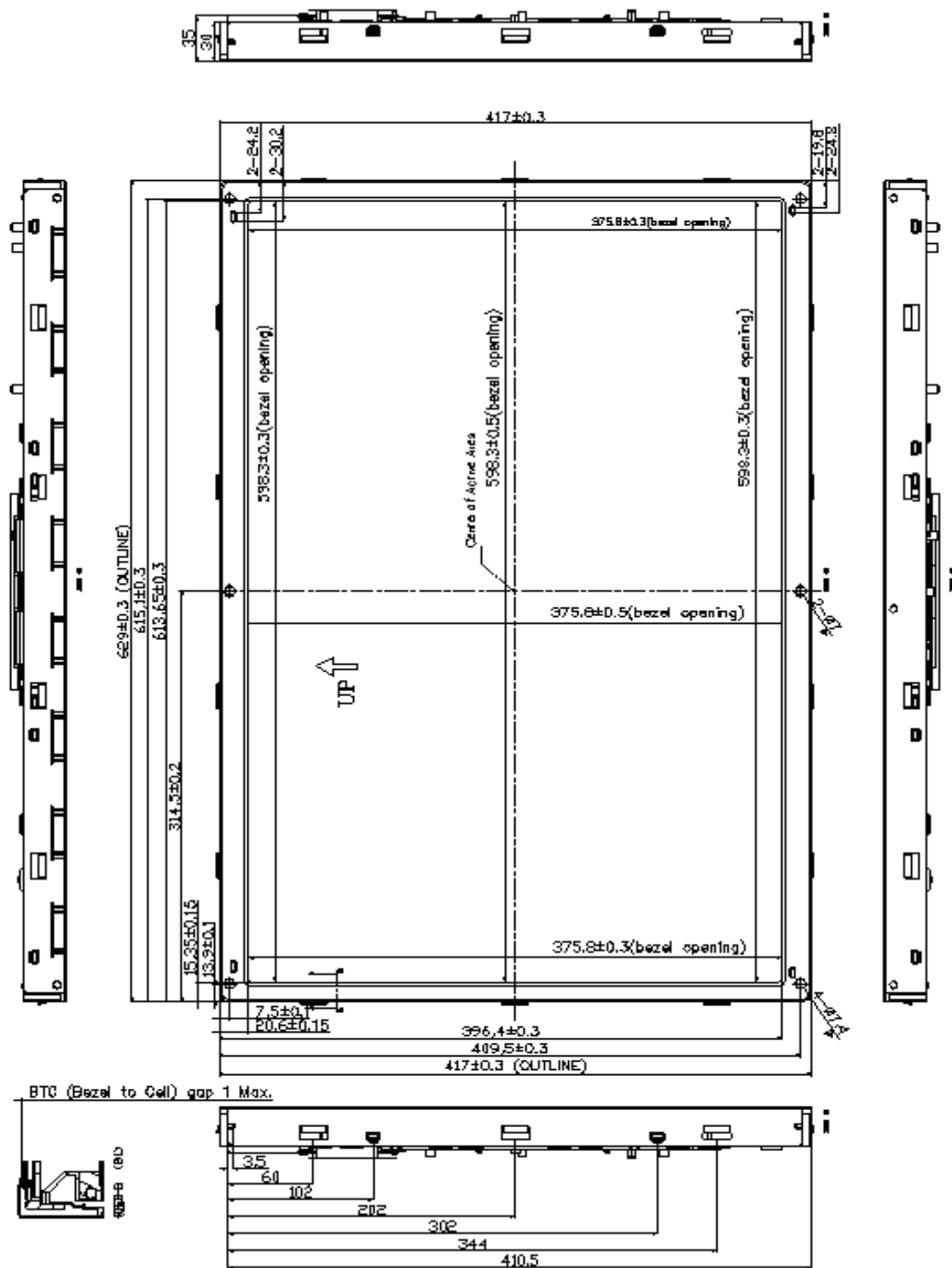
(2) $V_{LCD} < 3.5V$

V_{LCD} - dip conditions should also follow the power On/Off conditions for supply voltage.

Document Title	HSD280MUW3 Standard Specification	Page No.	24 / 29
Document No.	DC130-004707	Revision	1.0

7.0 OUTLINE DIMENSION

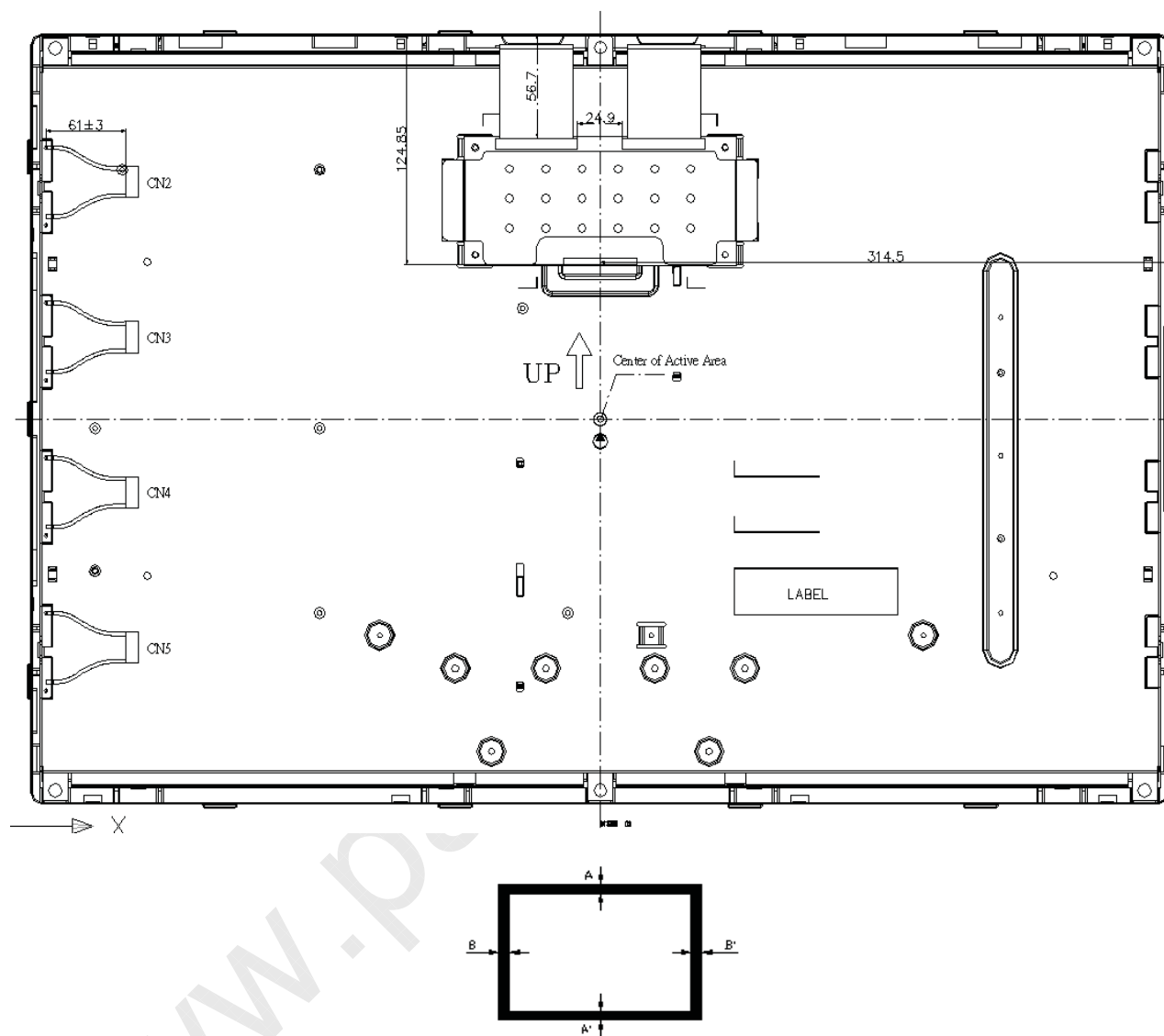
7.1 Front View:



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Document Title	HSD280MUW3 Standard Specification	Page No.	25 / 29
Document No.	DC130-004707	Revision	1.0

7.2 Back View:



BM Assembly Tolerance

$$|A-A'| \leq 1.5 \text{ (mm)}$$

$$|B-B'| \leq 1.5 \text{ (mm)}$$

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HannStar Display Corp.

Document Title	HSD280MUW3 Standard Specification	Page No.	26 / 29
Document No.	DC130-004707	Revision	1.0

8.0 LOT MARK

8.1 Lot Mark

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
---	---	---	---	---	---	---	---	---	----	----	----	----	----	----

code 1,2,3,4,5,6: HannStar internal flow control code.

code 7: production location.

code 8: production year.

code 9: production month.

code 10,11,12,13,14,15: serial number.

Note (1) Production Year: Code 8 is defined by the last number of the year. For example:

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

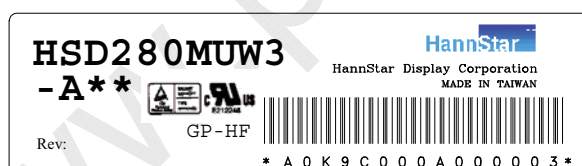
Note (2) Production Month

Month	Jan.	Feb.	Mar.	Apr.	May.	Jun.	Jul.	Aug.	Sep.	Oct	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	A	B	C

8.2 Location of Lot Mark

(1) The label is attached to the backside of the LCD module.

(2) This is subject to change without prior notice.



Lot mark

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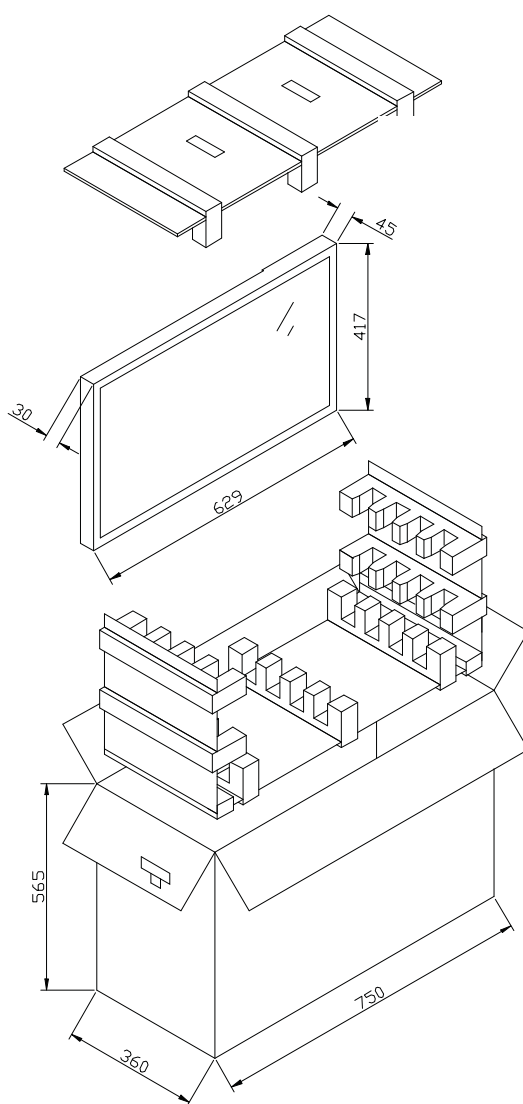
Document Title	HSD280MUW3 Standard Specification	Page No.	27 / 29
Document No.	DC130-004707	Revision	1.0

9.0 PACKAGE SPECIFICATION

9.1 Packing form

- (1) package quantity in one carton: 4 pieces.
- (2) carton size: $746\pm 3\text{ mm}\times 356\pm 3\text{ mm}\times 553^{\text{H}}\pm 3\text{ mm}$.
- (3) for domestic transportation only.

9.2 Packing assembly drawings



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HannStar Display Corp.

Document Title	HSD280MUW3 Standard Specification	Page No.	28 / 29
Document No.	DC130-004707	Revision	1.0

10.0 GENERAL PRECAUTION

10.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

10.2 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. HannStar does not warrant the module, if customers disassemble or modify the module.

10.3 Breakage of LCD Panel

10.3.1 If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.

10.3.2 If liquid crystal contacts mouth or eyes, rinse out with water immediately.

10.3.3 If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.

10.3.4 Handle carefully with chips of glass that may cause injury, when the glass is broken.

10.4 Electric Shock

10.4.1 Disconnect power supply before handling LCD module.

10.4.2 Do not pull or fold the CCFL cable.

10.4.3 Do not touch the parts inside LCD modules and the fluorescent lamp's connector or cables in order to prevent electric shock.

10.5 Absolute Maximum Ratings and Power Protection Circuit

10.5.1 Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.

10.5.2 Please do not leave LCD module in the environment of high humidity and high temperature for a long time.

10.5.3 It's recommended employing protection circuit for power supply.

10.6 Operation

10.6.1 Do not touch, push or rub the polarizer with anything harder than HB pencil lead. Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.

10.6.2 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.

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HannStar Display Corp.

Document Title	HSD280MUW3 Standard Specification	Page No.	29 / 29
Document No.	DC130-004707	Revision	1.0

10.6.3 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.

11.6.4 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzene or other adequate solvent.

10.7 Mechanism

Please mount LCD module by using mounting holes arranged in four corners tightly.

10.8 Static Electricity

10.8.1 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.

10.8.2 Because LCD module uses CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge.

10.8.3 Persons who handle the module should be grounded through adequate methods.

10.9 Strong Light Exposure

The module shall not be exposed under strong light such as direct sunlight. Otherwise, display characteristics may be changed.

10.10 Disposal

When disposing LCD module, obey the local environmental regulations.

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